

## 800V N-Channel Super-Junction MOSFET Gen-II

### Description

SJ-FET is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance.

This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy.

SJ-FET is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

### Features

- Multi-Epi process SJ-FET
- 850V @TJ = 150 °C
- Typ. RDS(on) = 0.138Ω
- Ultra Low Gate Charge (typ. Qg = 57.5nC)
- 100% avalanche tested
- Integrated Zener diode for high ESD robustness(>2kV HBM)



### Package Marking and Ordering Information:

Marking	Package	Part #	Hazardous Substance Control	Packing
SR80R160F	T0-220F-3L	SR80R160F	Pb free	Tube
SR80R160T	T0-220-3L	SR80R160T	Pb free	Tube

## Absolute Maximum Ratings

Symbol	Parameter	SR80R160T	SR80R160F	Unit
V <sub>DSS</sub>	Drain-Source Voltage	800		V
I <sub>D</sub>	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	25*		A
		15.8*		
I <sub>DM</sub>	Drain Current - Pulsed (Note 1)	75		A
V <sub>GSS</sub>	Gate-Source voltage	±30		V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	960		mJ
I <sub>AS</sub>	Avalanche current, repetitive or not-repetitive (pulse width limited by T <sub>J</sub> max)	8		A
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15		V/ns
dV <sub>ds</sub> /dt	Drain Source voltage slope (V <sub>ds</sub> =480V)	50		V/ns
P <sub>D</sub>	Power Dissipation (TC = 25°C)	190	37	W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150		°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering Purpose, 1/16" from Case for 10 Seconds	260		°C

\*Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75

## Thermal Characteristics

Symbol	Parameter	SR80R160T	SR80R160F	Unit
R <sub>θJC</sub>	Thermal Resistance, Junction-to-Case	0.65	3.4	°C/W
R <sub>θCS</sub>	Thermal Resistance, Case-to-Sink Typ.	0.5	-	°C/W
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient	62	80	°C/W

**Electrical Characteristics TC = 25°C** unless otherwise noted

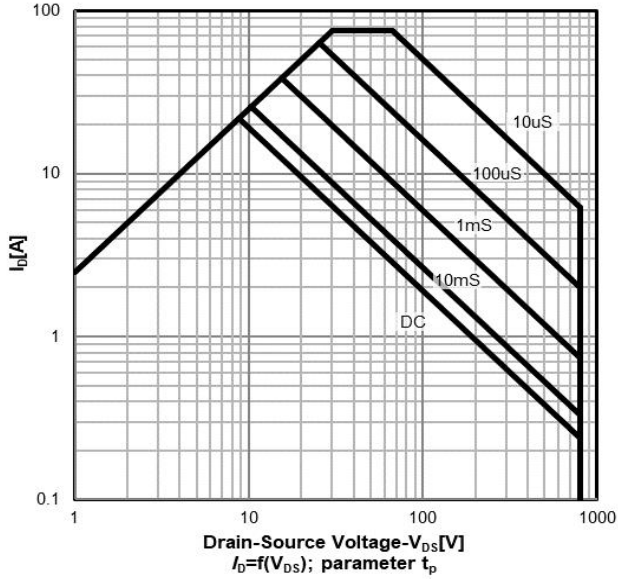
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 25°C	800	-	-	V
		V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA, T <sub>J</sub> = 150°C	850	-	-	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250μA, Referenced to 25°C	-	0.75	-	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 800V, V <sub>GS</sub> = 0V -T <sub>J</sub> = 125°C	-	-	1 100	μA μA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 20V, V <sub>DS</sub> = 0V	-	-	1	μA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -20V, V <sub>DS</sub> = 0V	-	-	-1	μA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	3.0	4.0	5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 12A	-	0.138	0.16	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	2850	-	pF
C <sub>oss</sub>	Output Capacitance		-	90	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	2.4	-	pF
E <sub>oss</sub>	Stored Energy in Output Capacitance		-	10.9	-	μJ
C <sub>o(er)</sub>	Energy Related Output Capacitance	V <sub>DS</sub> = 0 to 600V, V <sub>GS</sub> = 0V	-	61	-	pF
C <sub>o(tr)</sub>	Time Related Output Capacitance		-	240	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 400V, I <sub>D</sub> = 12A, V <sub>GS</sub> = 10V (Note 4)	-	57.5	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	15.8	-	nC
Q <sub>gd</sub>	Gate-Drain Charge		-	19.5	-	nC
V <sub>plateau</sub>	Gate plateau voltage		-	5.6	-	V
R <sub>g</sub>	Gate resistance	f=1 MHz, open drain	-	4.5	-	Ω
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> = 400V, I <sub>D</sub> = 12A R <sub>G</sub> = 4.7Ω, V <sub>GS</sub> = 10V (Note 4)	-	27	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	18	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		-	89	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	15	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		-	-	25	A
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		-	-	75	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 25A	-	0.9	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 400V, I <sub>S</sub> = 12A, dI <sub>F</sub> /dt = 100A/μs	-	400	-	ns
Q <sub>rr</sub>	Reverse Recovery Charge		-	6.4	-	μC
I <sub>rrm</sub>	Peak Reverse Recovery Current		-	30	-	A

**NOTES:**

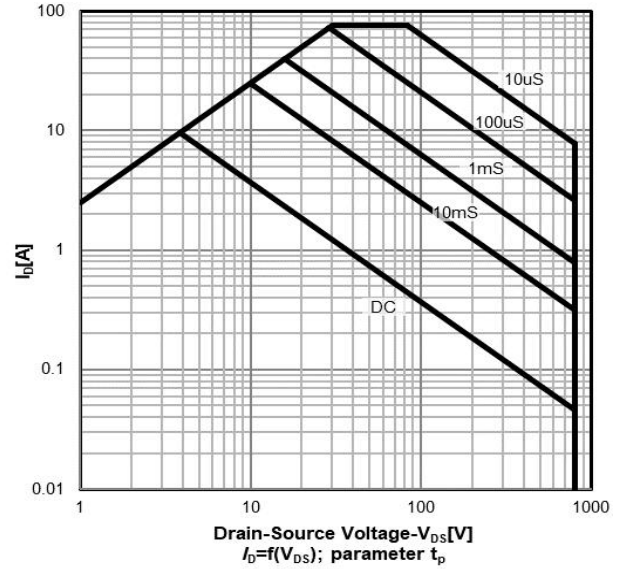
- 1.Repetitive Rating: Pulse width limited by maximum junction temperature
- 2.ID=IAS, VDD=50V, L=30mH, Starting TJ=25 °C
- 3.ISD≤ID, di/dt ≤ 200A/us, VDD ≤ BVDSS, Starting TJ = 25 °C
- 4.Essentially Independent of Operating Temperature Typical Characteristics

## Typical Performance Characteristics

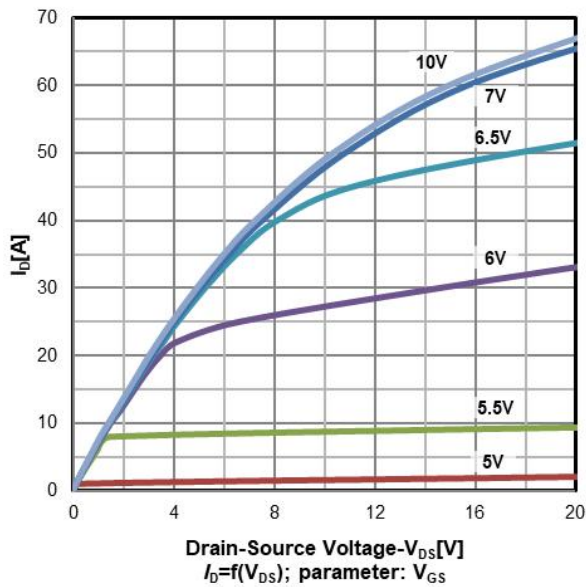
Typ. Safe operating area TC=25 °C  
TO-220/TO-252



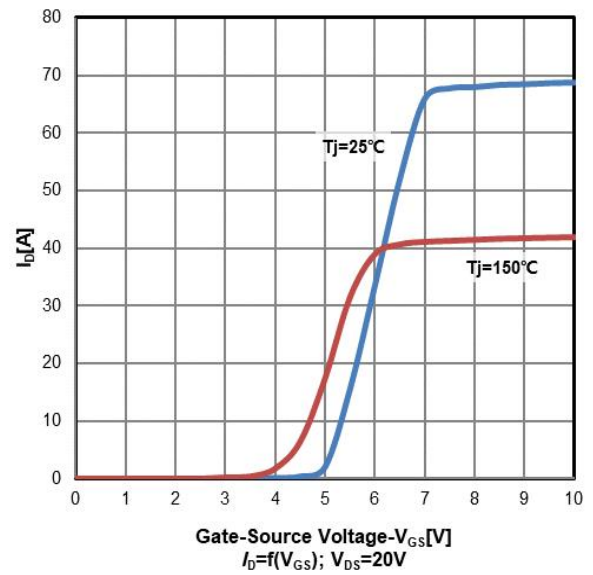
Typ. Safe operating area TC=25 °C  
TO-220FullPAK



Typ. output characteristics  $T_j=25\text{ °C}$

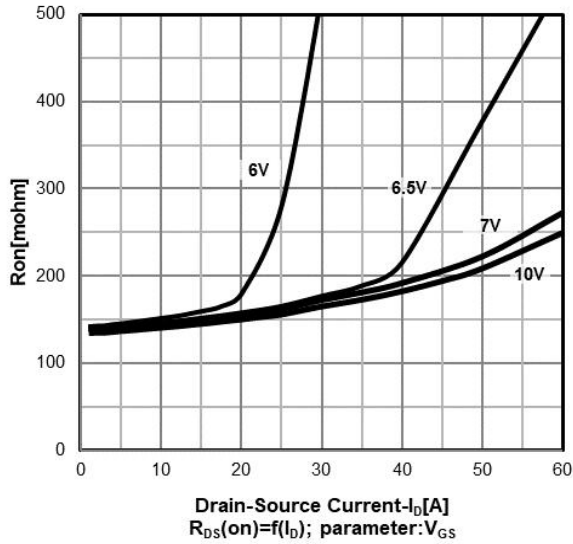


Typ. Transfer characteristics

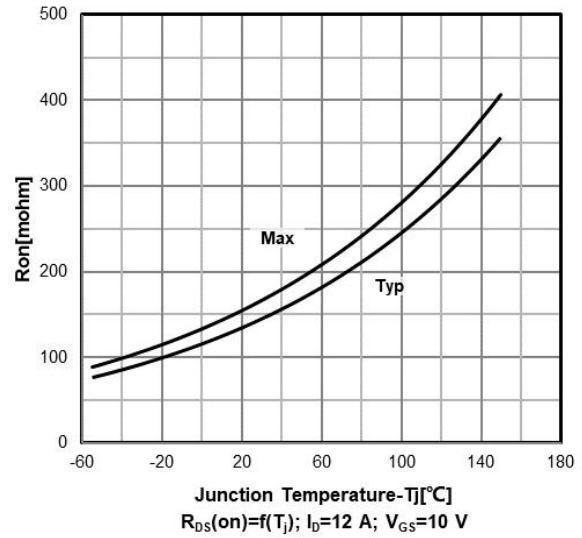


## Typical Performance Characteristics

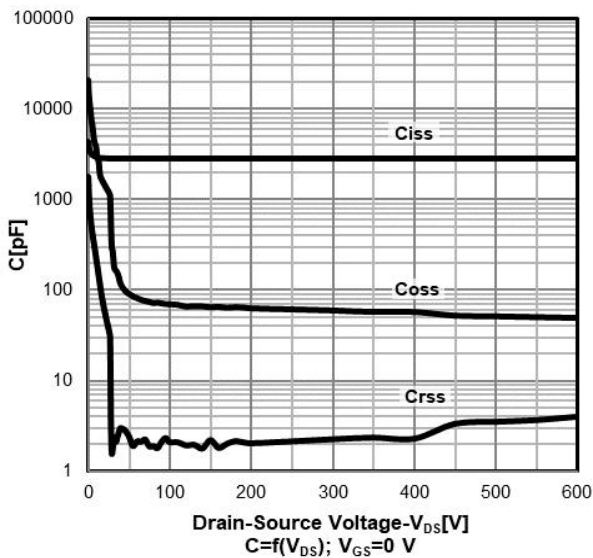
Typ. drain-source on-state resistance



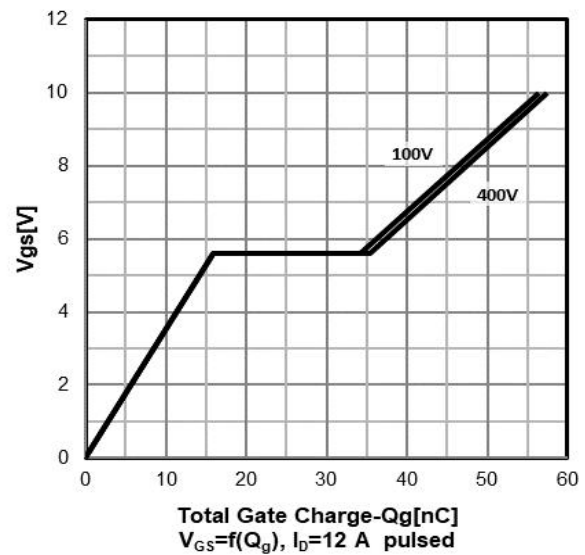
Typ. On-resistance vs temperature



Typ. capacitances

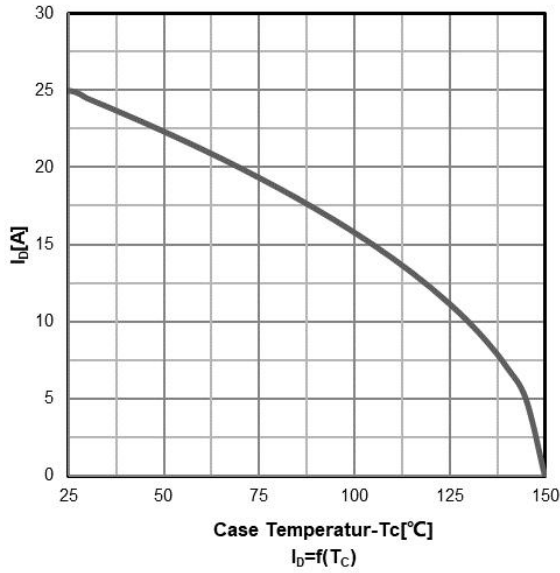


Typ. gate charge characteristics

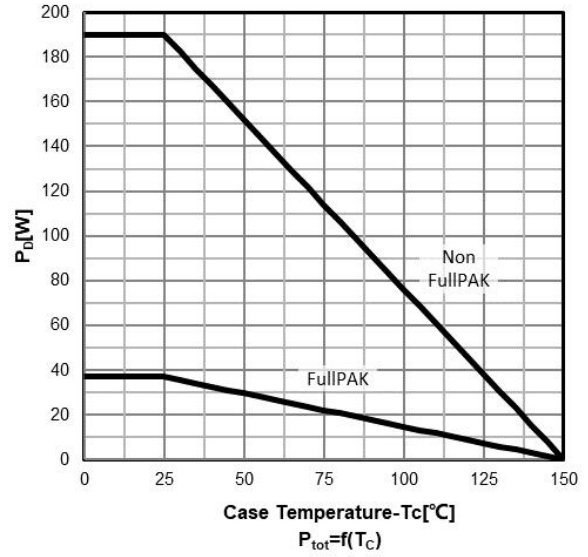


## Typical Performance Characteristics

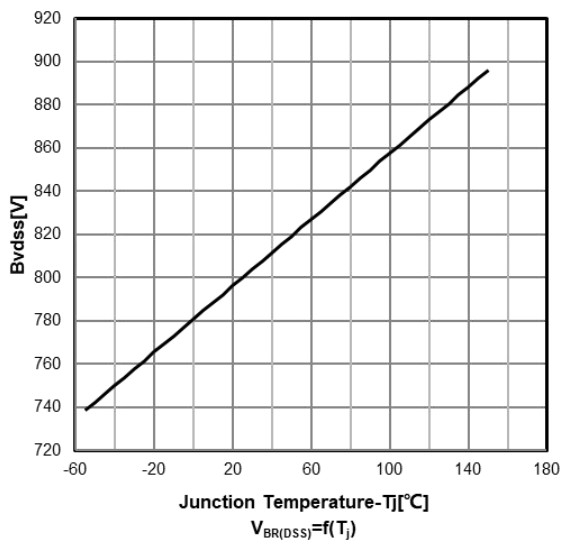
Typ. Drain current vs temperature



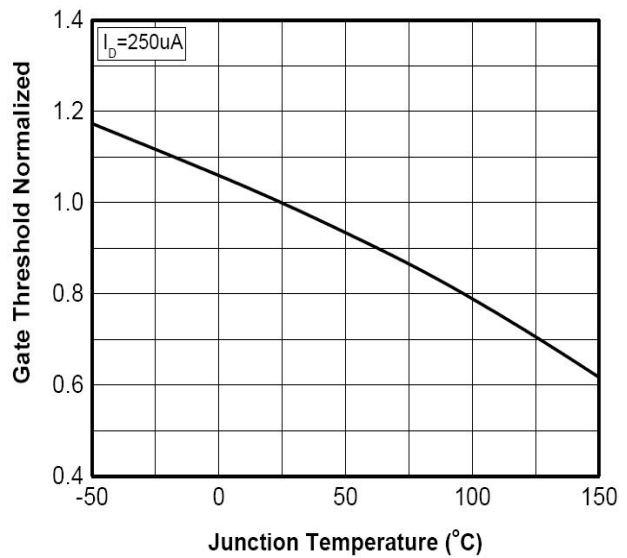
Typ. Power dissipation



Typ. Drain-source breakdown voltage

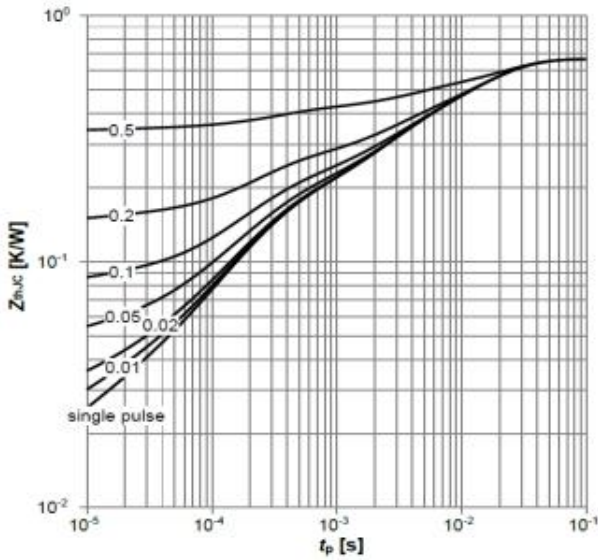


Typ. Normalized VGS(th) characteristics

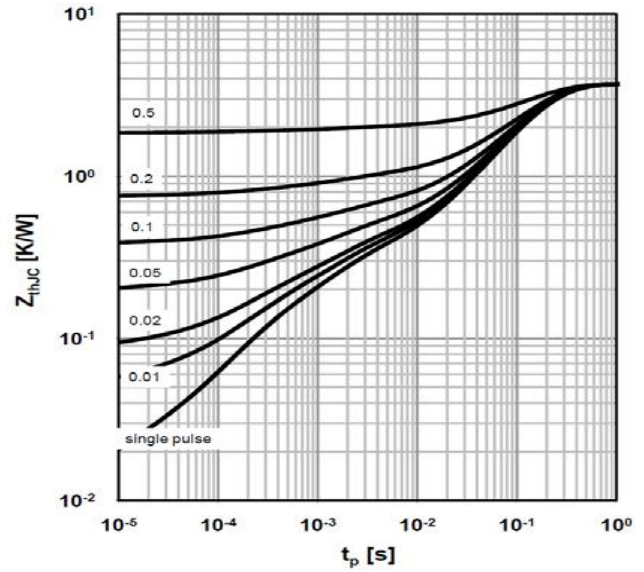


## Typical Performance Characteristics

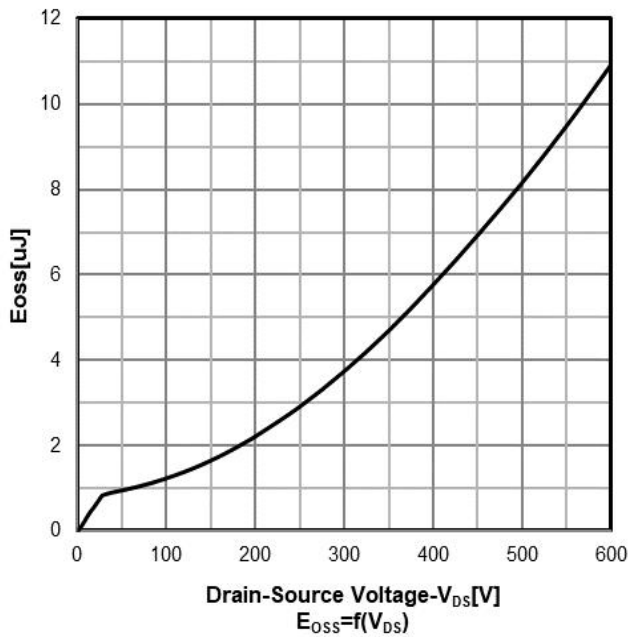
Max. transient thermal impedance TO-220



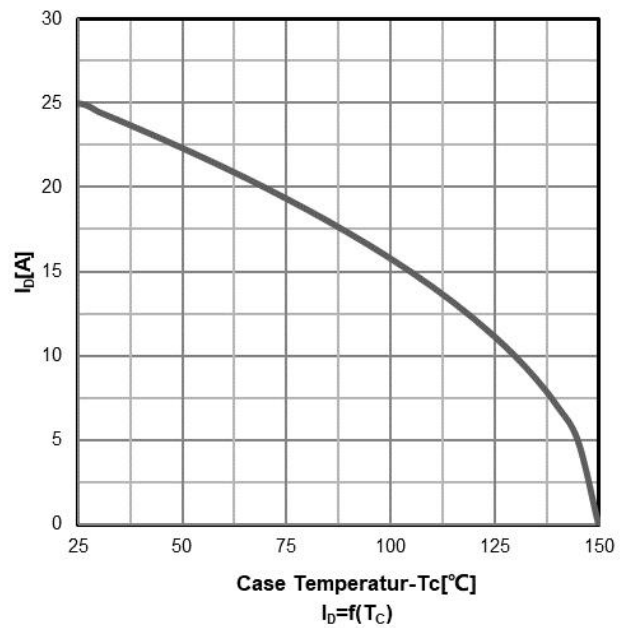
Max. transient thermal impedance TO-220FullPAK



Typ.Coss stored energy



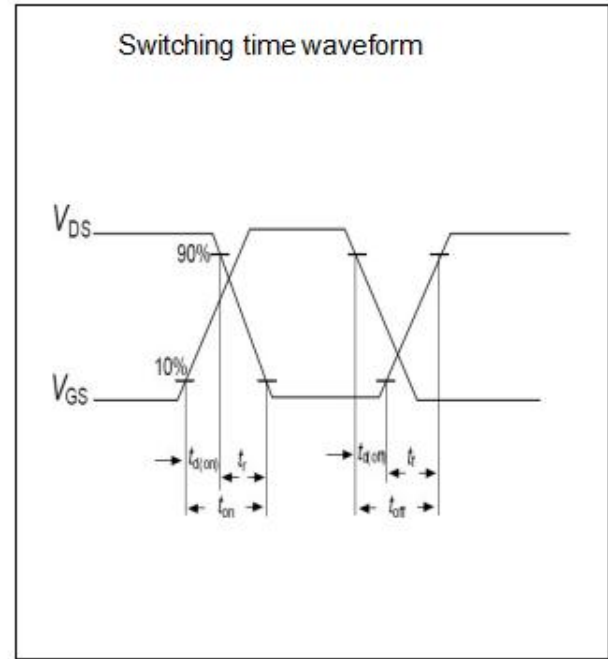
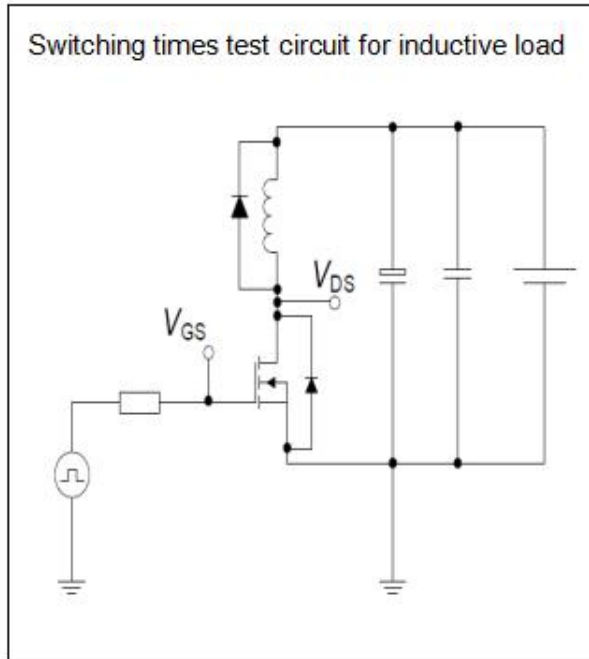
Typ.Forward characteristics of reverse diode



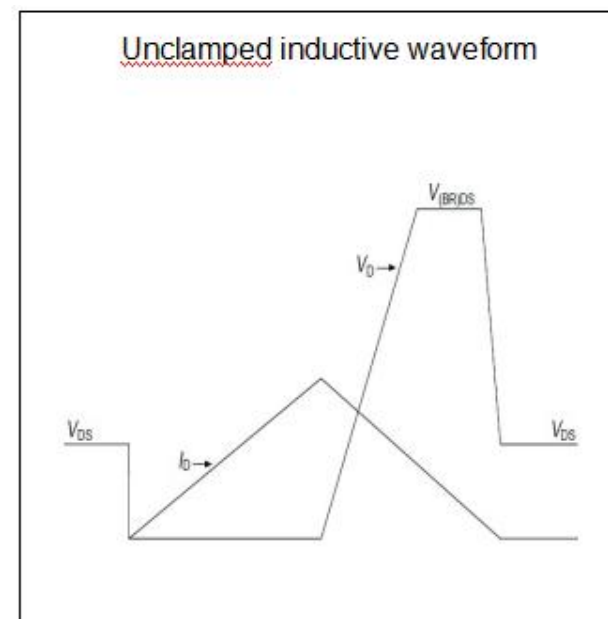
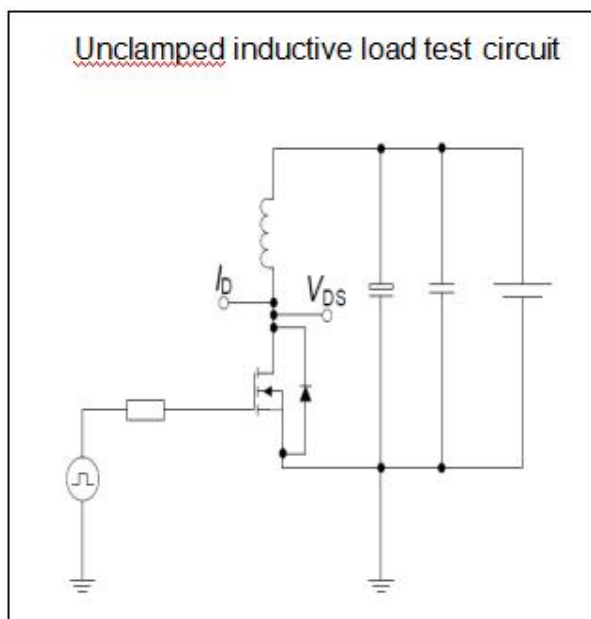


## Test circuits

### Switching times test circuit and waveform for inductive load

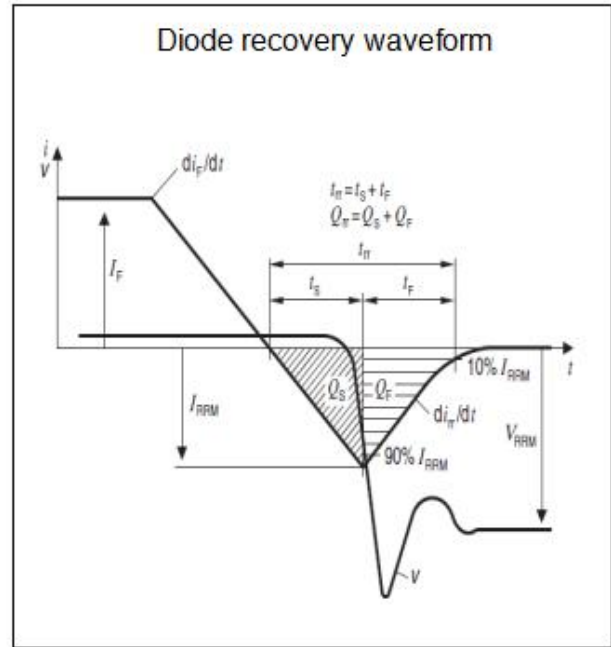
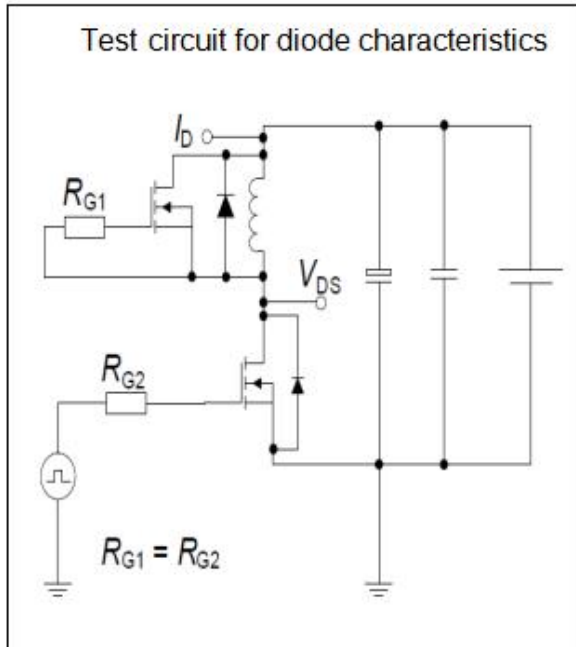


### Unclamped inductive load test circuit and waveform



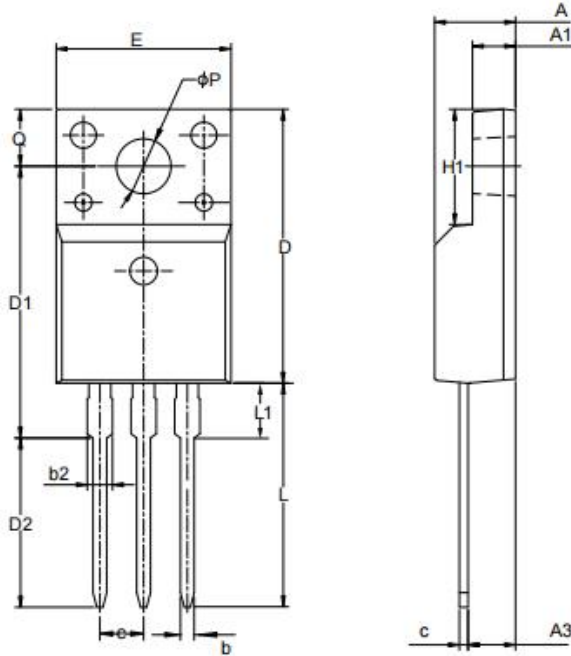
## Test circuits

### Test circuit and waveform for diode characteristics



## Package Outline

### TO-220 Full PAK



COMMON DIMENSIONS			
Items	Values(mm)		
	MIN	NOM	MAX
A	4.42	4.7	5.02
A1	2.3	2.54	2.8
A3	2.5	2.76	3.1
b	0.7	0.8	0.9
b2	--	--	1.47
c	0.35	0.5	0.65
D	15.25	15.87	16.25
D1	15.3	15.75	16.3
D2	9.3	9.8	10.3
E	9.73	10.16	10.36
e	2.54BSC		
H1	6.4	6.68	7
L	12.48	12.98	13.48
L1	--	--	3.5
øP	3	3.18	3.4
Q	3.05	3.3	3.55

### TO-220-3L

COMMON DIMENSIONS			
Items	Values(mm)		
	MIN	NOM	WAX
A	4.3	4.5	4.7
A1	1	1.3	1.5
A2	1.8	2.4	2.8
b	0.6	0.8	1
b1	1	-	1.6
c	0.3	-	0.7
D	15.1	15.7	16.1
D1	8.1	9.2	10
F	9.6	9.9	10.4
e	2.54BSC		
H1	6.1	6.5	7
L	12.6	13.08	13.6
L1			3.95
ΦP	3.4	3.7	3.9
Q	2.6		3.2

